

Specification for Approval

Customer: _____

Model Name: _____

| Supplier Approval | | | Customer approval |
|-------------------|-----------------|-------------|-------------------|
| R&D Designed | R&D Approved | QC Approved | |
| <i>Peter</i> | <i>Peng Jun</i> | | |

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

2. General Information

| ITEM | STANDARD VALUES | UNITS |
|--------------------------------|-------------------------------|-------|
| LCD type | 2.0" TFT | -- |
| Dot arrangement | 176(RGB) × 220 | dots |
| Color filter array | RGB vertical stripe | -- |
| Display mode | Normally white TN | - |
| Gray Scale Inversion Direction | 6 O'clock | -- |
| Eyes Viewing Direction | 12 O'clock | -- |
| Driver IC | ST7775R | -- |
| Module size | 37.63(W) × 51.25(H) × 2.45(T) | mm |
| Active area | 31.68(W) × 39.60(H) | mm |
| Dot pitch | 0.180 (W) × 0.180(H) | mm |
| Interface | 8/16-bit MCU interface | -- |
| Operating temperature | -20 ~ +70 | °C |
| Storage temperature | -30 ~ +80 | °C |
| Back Light | 3 White LED | -- |
| Weight | TBD | g |

3. External Dimensions

NOTES:

1. DISPLAY TYPE: 2.0" TFT
2. VIEWING DIRECTION: 12:00
3. LCD DRIVE IC: ST7775R
4. OPERATING TEMP: -20° C ~ +70° C
5. STORAGE TEMP: -30° C ~ +80° C
6. UNMARKER TOLERANCE: ±0.20
7. REQUIREMENTS ON ENVIRONMENTAL PROTECTION: RoHS
8. LED COLOR: WHITE; 3PCS CHIP; VLED=3.2V TYP
IF=60mA (CONSTANT CURRENT)

51.25 OUTLINE
46.20 LCD DD
39.60 LCD AA
1.27
1.40±0.8
13.18±0.8
1.80±0.3
34.03
0.9*35=31.50±0.1
0.45
36
1.0
D.S.T
TFT 2.0"
176*86*220
VIEW DIRECTION

2.45±0.2
3-0.60
2-3.30±0.5
1.50±0.3
1.40
31.23
3-φ0.75±0.1
2-φ0.80±0.1
36
36

50.30±0.1
(0.48)
36.68±0.1
(0.48)
2-1.50±0.8
2-37.00±0.8
D.S.T (T=0.2mm)
COMPONENT AREA
36
36

LEDK1
LEDK2
LEDK3

BL CIRCUIT DIAGRAM

| NO. | Pin Name |
|-----|----------|
| 1 | NC |
| 2 | NC |
| 3 | NC |
| 4 | NC |
| 5 | DNC |
| 6 | NCS |
| 7 | DRESET |
| 8 | NRD |
| 9 | GND |
| 10 | NWR |
| 11 | D15 |
| 12 | D14 |
| 13 | D13 |
| 14 | D12 |
| 15 | D11 |
| 16 | D10 |
| 17 | D8 |
| 18 | D8 |
| 19 | GND |
| 20 | D7 |
| 21 | D6 |
| 22 | D5 |
| 23 | D4 |
| 24 | D3 |
| 25 | D2 |
| 26 | D1 |
| 27 | D0 |
| 28 | IMO |
| 29 | TE |
| 30 | GND |
| 31 | IOVCC |
| 32 | VCC |
| 33 | LED_A |
| 34 | LED_K1 |
| 35 | LED_K2 |
| 36 | LED_K3 |

| SMV. | DESCRIPTION OF REVISION | REASON | REVISED BY | DATE |
|------|-------------------------|--------|------------|------------|
| △ | first issue | A | | 2017/06/27 |

| | | | |
|--------------------------|---|------------|------------------|
| | 于都上晴电子有限公司 YU DU AMSON ELECTRONICS Co., Ltd. | | |
| TITLE: OUTLINE DIMENSION | D/N: AM176220-020H | UNIT: mm | |
| DRAWN BY: | REV: A | SCALE: 1:1 | SHEET NO: 1 OF 1 |
| CHECKED BY: | | | |
| APPROVED BY: | | | |

4. Interface Description

| PIN NO. | PIN NAME | DESCRIPTION |
|---------|----------|--|
| 1 | NC | NC |
| 2 | NC | NC |
| 3 | NC | NC |
| 4 | NC | NC |
| 5 | DNC | Register select signal, low : Index , high :register. |
| 6 | NCS | Chip selection, Active low. |
| 7 | NRESET | Reset pin, Active low. |
| 8 | NRD | Read execution control pin, Active low. |
| 9 | GND | Ground |
| 10 | NWR | Write execution control pin, Active low. |
| 11 | D15 | Data bus. |
| 12 | D14 | Data bus. |
| 13 | D13 | Data bus. |
| 14 | D12 | Data bus. |
| 15 | D11 | Data bus. |
| 16 | D10 | Data bus. |
| 17 | D9 | Data bus. |
| 18 | D8 | Data bus. |
| 19 | GND | Ground |
| 20 | D7 | Data bus. |
| 21 | D6 | Data bus. |
| 22 | D5 | Data bus. |
| 23 | D4 | Data bus. |
| 24 | D3 | Data bus. |
| 25 | D2 | Data bus. |
| 26 | D1 | Data bus. |
| 27 | D0 | Data bus. |
| 28 | IM0 | The MCU interface mode select IM0=0 80-16bit D0~D15 IM0=1 80-8bit D8~D15 |
| 29 | TE | Tearing effect output pin to synchronies MCU to frame writing, activated by S/W command. |
| 30 | GND | Ground. |
| 31 | IOVCC | Logic IO power supply. |
| 32 | VCC | Analog power supply. |
| 33 | LEDA | Anode of LED. |
| 34 | LEDK1 | Cathode of LED. |
| 35 | LEDK2 | Cathode of LED. |
| 36 | LEDK3 | Cathode of LED. |

5. Absolute Maximum Ratings

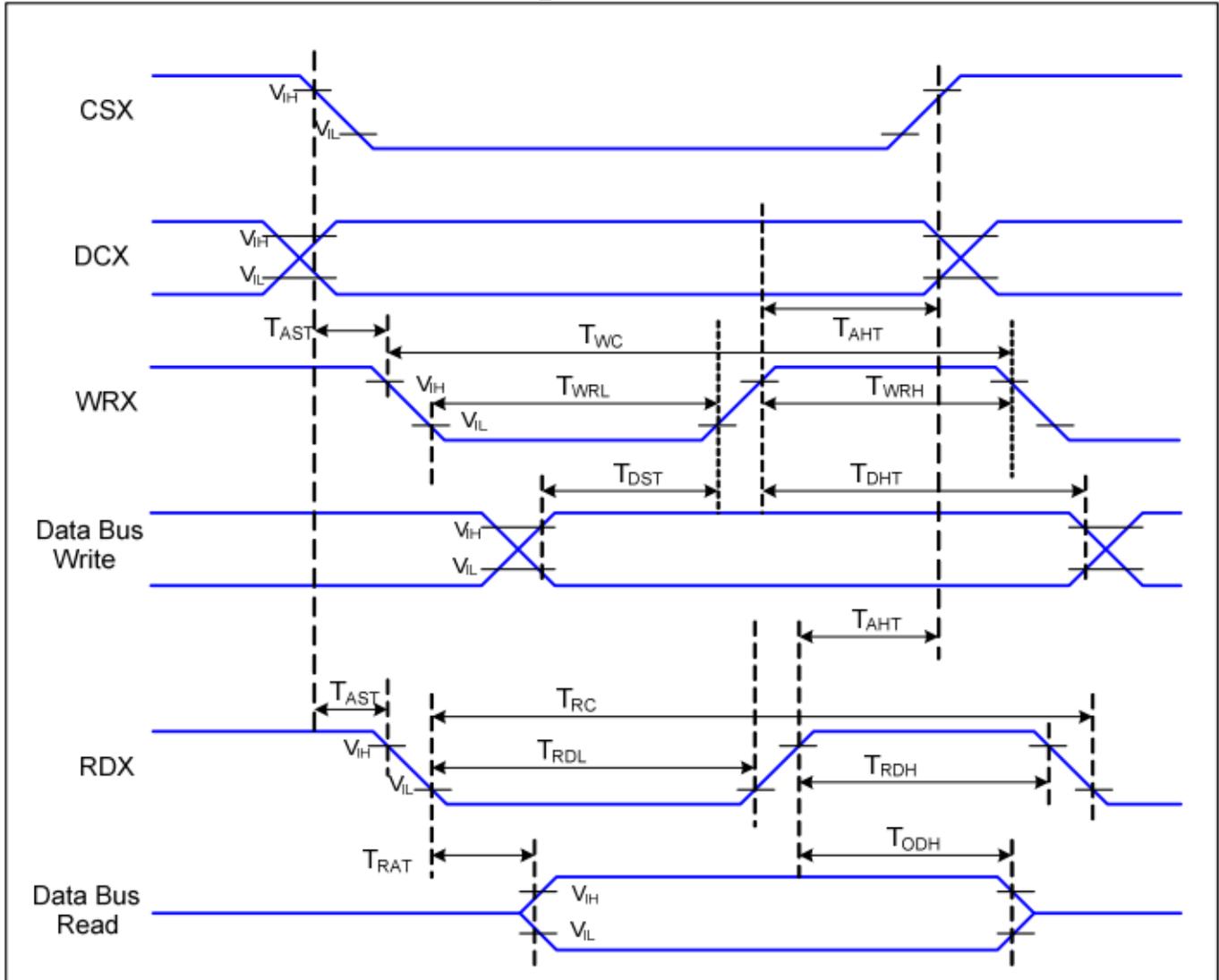
| Item | Symbol | Min. | Max. | Unit |
|-----------------------|-----------------|------|------|------|
| Logic Supply Voltage | IOVCC | -0.3 | 4.6 | V |
| Analog Supply Voltage | VCI | -0.3 | 4.6 | V |
| Operating Temperature | T _{OP} | -20 | 70 | °C |
| Storage Temperature | T _{ST} | -30 | 80 | °C |
| Storage Humidity | HD | 20 | 90 | %RH |

6. DC Characteristics

| Item | Symbol | Min. | Typ. | Max. | Unit | Remark |
|-----------------------|-----------------|----------|------|----------|------|--------|
| Logic Supply Voltage | IOVCC | 1.65 | 1.8 | 3.3 | V | - |
| Analog Supply Voltage | VCC | 2.5 | 2.8 | 3.3 | V | - |
| Input High Voltage | V _{IH} | 0.8IOVCC | - | IOVCC | V | - |
| Input Low Voltage | V _{IL} | GND | - | 0.2IOVCC | V | - |
| Output High Voltage | V _{OH} | 0.8IOVCC | - | IOVCC | V | - |
| Output Low Voltage | V _{OL} | GND | - | 0.2IOVCC | V | - |
| I/O Leak Current | I _{LI} | -1 | - | 1 | uA | - |

7. Timing Characteristics

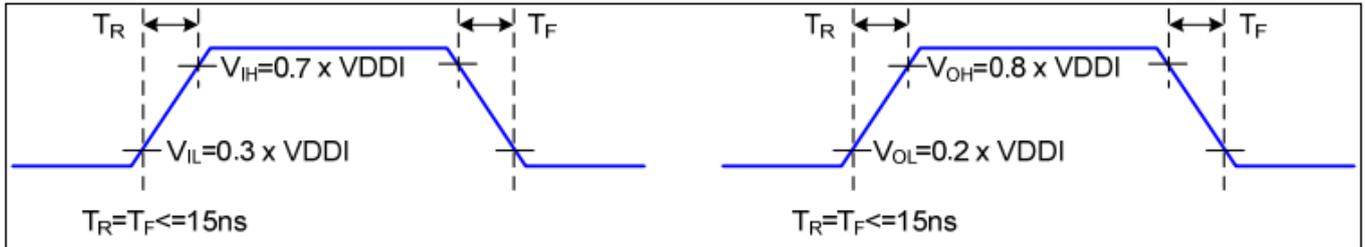
7.1.1 Parallel 8080 Interface Timing Characteristics



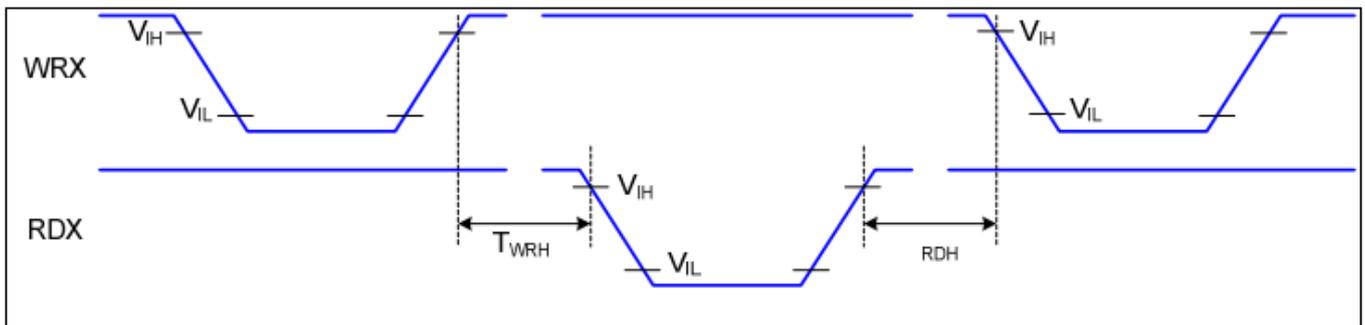
| Signal | Symbol | Parameter | Min | Max | Unit | Description |
|----------|--------|---------------------------------|-----|-----|------|--------------------------------------|
| DCX | TAST | Address Setup Time | TBD | | ns | |
| | TAHT | Address Hold Time (Write/Read) | TBD | | ns | |
| WRX | TWC | Write Cycle | TBD | | ns | |
| | TWRH | Control Pulse "H" Duration | TBD | | ns | |
| | TWRL | Control Pulse "L" Duration | TBD | | ns | |
| RDX | TRC | Read Cycle (ID) | TBD | | ns | When Read ID Data |
| | TRDH | Control Pulse "H" Duration (ID) | TBD | | ns | |
| | TRDL | Control Pulse "L" Duration (ID) | TBD | | ns | |
| DB[17:0] | TDST | Data Setup Time | TBD | | ns | TRAT, TRATFM: 3K ohm Pull up or Down |
| | TDHT | Data Hold Time | TBD | | ns | |

| Signal | Symbol | Parameter | Min | Max | Unit | Description |
|--------|--------|-----------------------|-----|-----|------|---|
| | TRAT | Read Access Time (ID) | | TBD | ns | and 30pF Parallel Cap. To GND. TODH: 3K ohm Pull up or Down. |
| | TODH | Output Disable Time | TBD | TBD | ns | |

Parallel Interface Characteristics

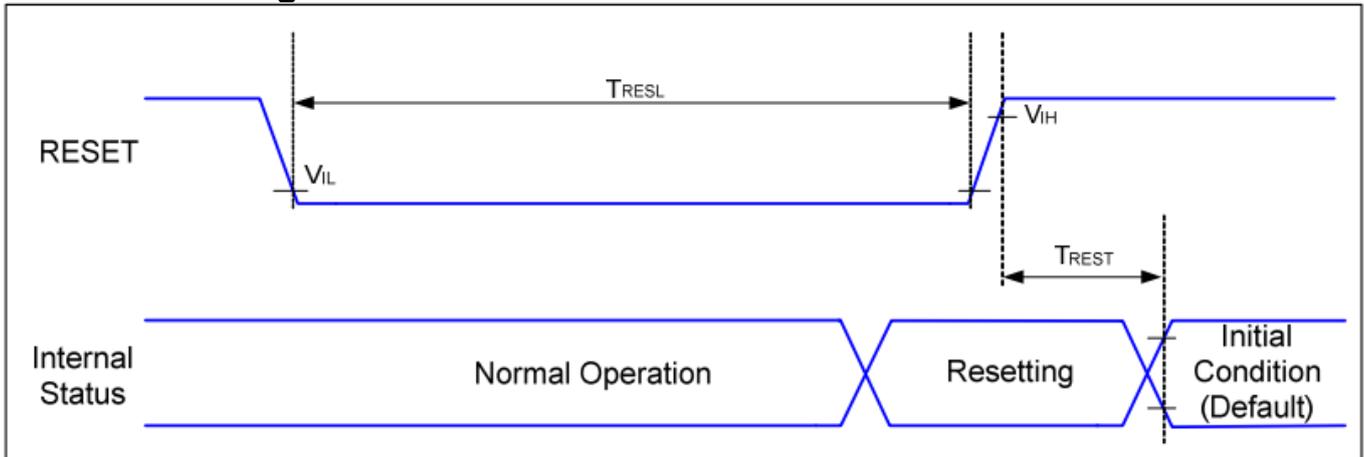


Rising and Falling Timing for I/O Signal



Write-to-Read and Read-to-Write Timing

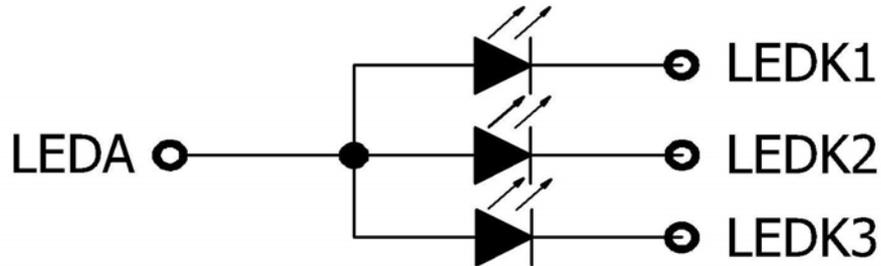
7.2 Reset Timing Characteristics



| Signal | Symbol | Parameter | Min | Max | Unit | Description |
|--------|--------|-----------------------|-----|-----|------|-------------|
| RESET | TRESL | Reset Low Level Width | 1 | - | ms | - |
| | TREST | Reset Complete Time | 1 | | ms | |

8. Backlight Characteristic

LED Circuit:

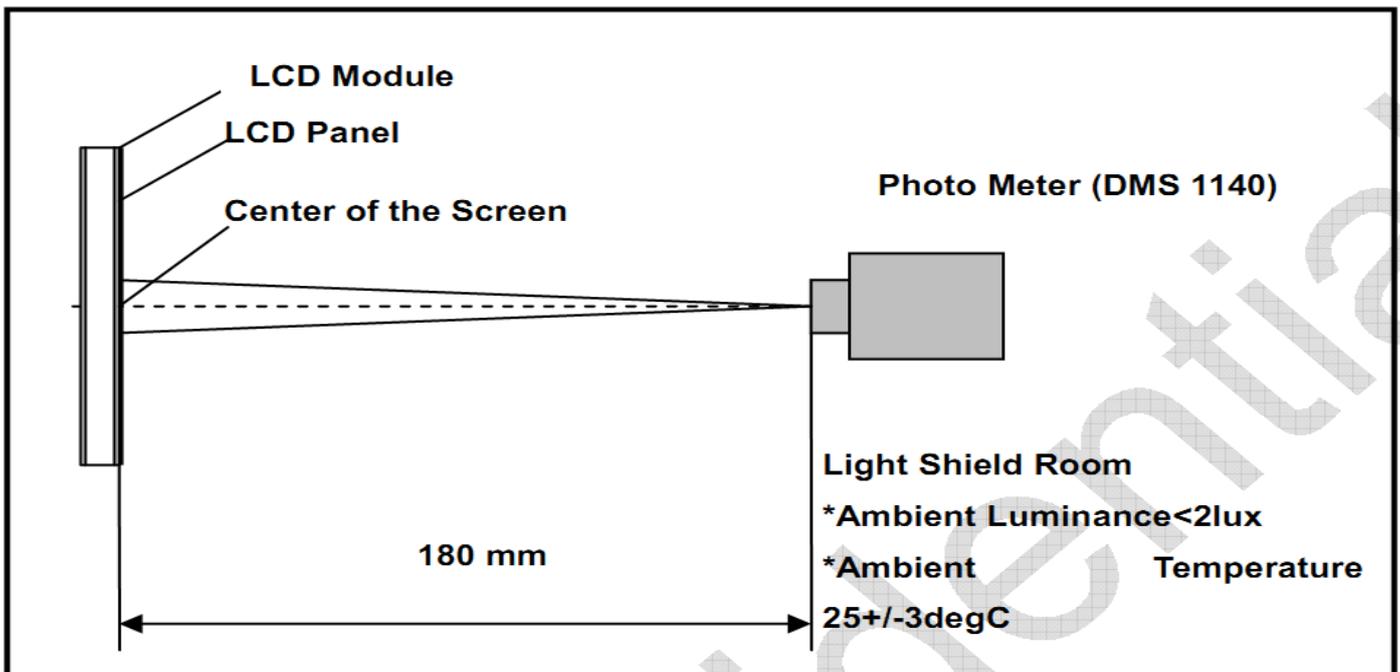


| Item | Symbol | MIN | TYP | MAX | UNIT | Test Condition |
|----------------------------|--------|-----|-------|-----|-------------------|----------------|
| Supply Voltage | Vf | 2.8 | 3.2 | 3.5 | V | If=45mA |
| Supply Current | If | - | 45 | - | mA | - |
| Luminous Intensity for LCM | - | 150 | 220 | - | cd/m ² | If=45mA |
| Uniformity for LCM | - | 80 | - | - | % | If=45mA |
| Life Time | - | - | 50000 | - | Hr | If=45mA |
| Backlight Color | White | | | | | |

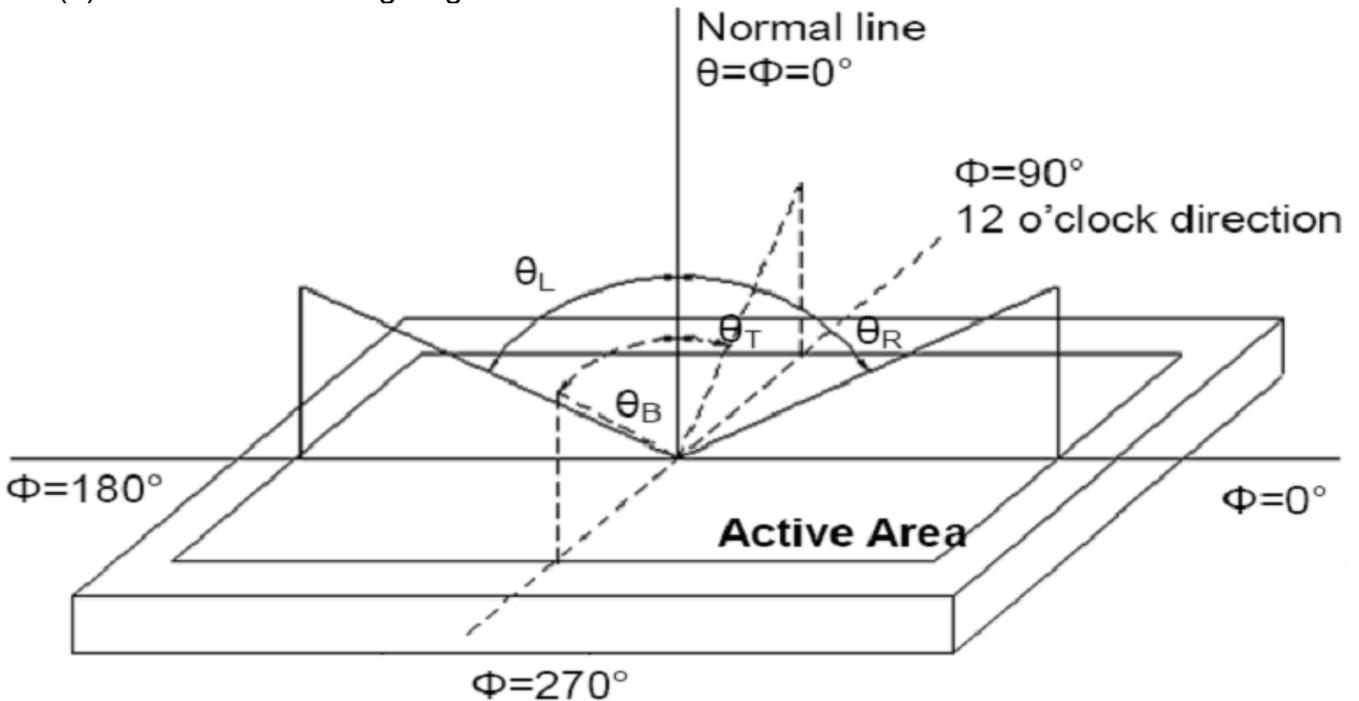
9. Optical Characteristics

| Item | Conditions | Min. | Typ. | Max. | Unit | Note | |
|------------------------------------|------------|---------------|------|---------------|------|-------------|-------------|
| Viewing Angle (CR>10) | Horizontal | θ_L | - | 45 | - | degree | (1),(2),(6) |
| | | θ_R | - | 45 | - | | |
| | Vertical | θ_T | - | 45 | - | | |
| | | θ_B | - | 20 | - | | |
| Contrast Ratio | Center | 200 | 300 | - | - | (1),(3),(6) | |
| Response Time | Rising | - | 20 | 40 | ms | (1),(4),(6) | |
| | Falling | | | | | | |
| CF Color Chromaticity (CIE1931) | Red x | Typ. -0.05 | TBD | Typ. +0.05 | - | (1), (6) | |
| | Red y | | TBD | | - | | |
| | Green x | | TBD | | - | | |
| | Green y | | TBD | | - | | |
| | Blue x | | TBD | | - | | |
| | Blue y | | TBD | | - | | |
| | White x | | TBD | | - | | |
| | White y | | TBD | | - | | |
| Transmittance | - | - | 5.5 | - | % | (1),(5),(6) | |

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



Note (2) Definition of Viewing Angle



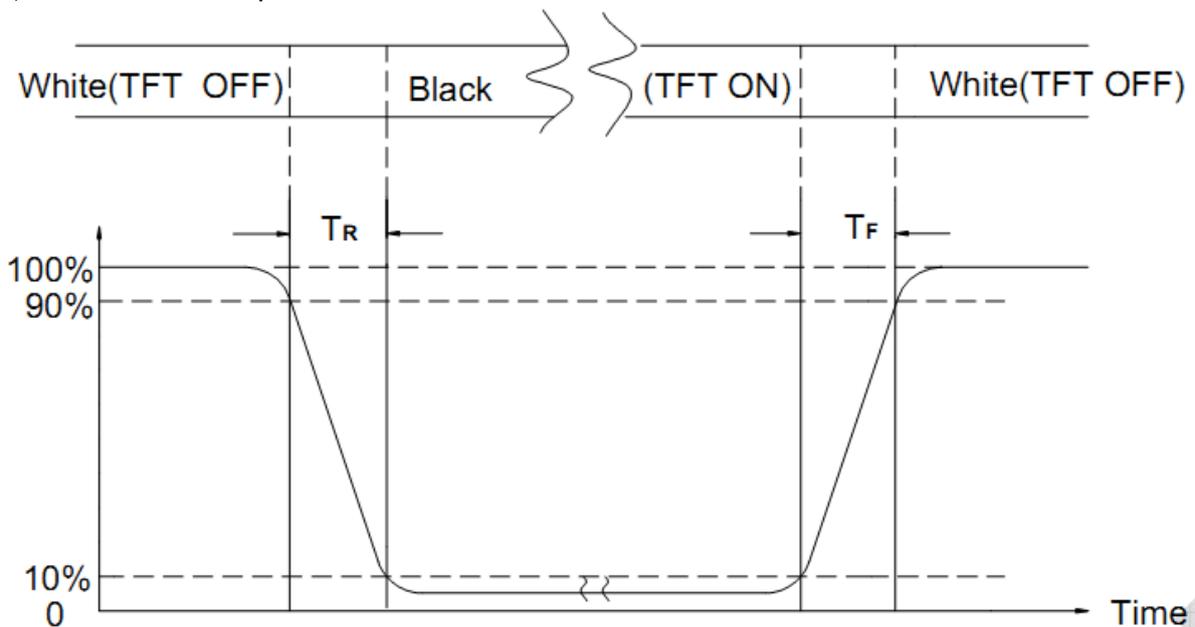
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

$$\text{Transmittance} = \text{Center Luminance of LCD} / \text{Center Luminance of Back Light} \times 100\%$$

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

10. Reliability Test Conditions And Methods

| NO. | TEST ITEMS | TEST CONDITION | INSPECTION AFTER TEST |
|-----|----------------------------|---|---|
| ① | High Temperature Storage | 80°C±2°C×96Hours | Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied. |
| ② | Low Temperature Storage | -30°C±2°C×96Hours | |
| ③ | High Temperature Operating | 70°C±2°C×96Hours | |
| ④ | Low Temperature Operating | -20°C±2°C×96Hours | |
| ⑤ | Temperature Cycle(Storage) | $ \begin{array}{ccccc} -20^{\circ}\text{C} & \longleftrightarrow & 25^{\circ}\text{C} & \longleftrightarrow & 70^{\circ}\text{C} \\ (30\text{min}) & & (5\text{min}) & & (30\text{min}) \\ & \longleftarrow & & \longrightarrow & \\ & & \text{1cycle} & & \\ & & \text{Total 10cycle} & & \end{array} $ | |
| ⑥ | Damp Proof Test (Storage) | 50°C±5°C×90%RH×120Hours | |
| ⑦ | Vibration Test | Frequency:10Hz~55Hz~10Hz Amplitude:1.5MM X,Y,Z direction for total 3hours (packing condition test will be tested by a carton) | |
| ⑧ | Drooping Test | Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton) | |
| ⑨ | ESD Test | Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times | |

REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4, In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11. Inspection Standard

11.1. QUALITY :

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

11.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 °C TO 40°C ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

11.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) , LEVEL II SINGLE PLAN.

| CLASS | AQL(%) |
|----------|--------|
| CRITICAL | 0.4 % |
| MAJOR | 0.65 % |
| MINOR | 1.5 % |
| TOTAL | 1.5 % |

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

11.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

11.2. CHECKING CONDITION

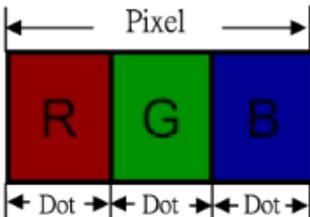
11.2.1. CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.

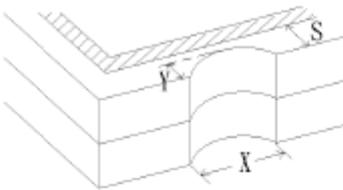
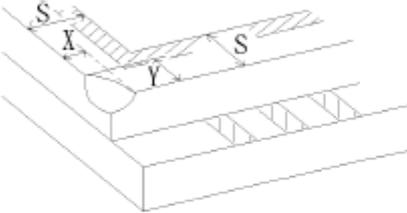
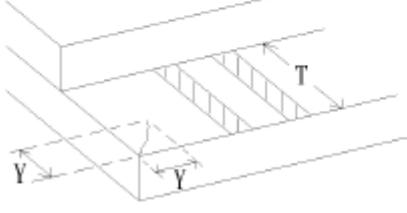
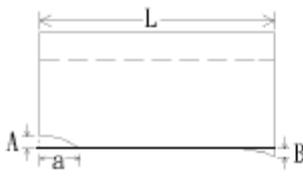
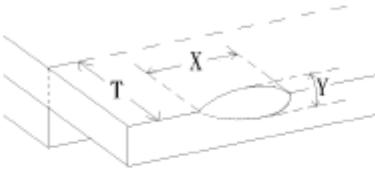
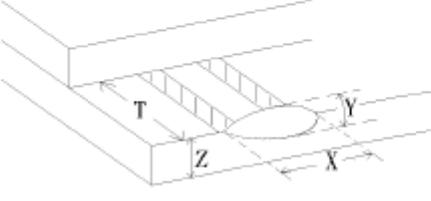
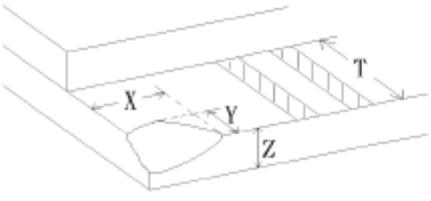
11.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

11.3. INSPECTION PLAN :

| CLASS | ITEM | JUDGEMENT | CLASS |
|--------------------|---|--|----------|
| PACKING & INDICATE | 1. OUTSIDE AND INSIDE PACKAGE | "MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE. | Minor |
| | 2. MODEL MIXED AND QUANTITY | OTHER MODEL MIXED.....REJECTED QUANTITY SHORT OR OVER.....REJECTED | Critical |
| | 3. PRODUCT INDICATION | "MODEL NO." SHOULD INDICATE ON THE PRODUCT | Major |
| ASSEMBLY | 4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT. | ACCORDING TO SPECIFICATION OR DRAWING. | Major |
| APPEARANCE | 5. VIEWING AREA | POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED | Minor |
| | 6. BLEMISH · BLACK SPOT · WHITE SPOT IN THE LCD AND LCD GLASS CRACKS | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| | 7. BLEMISH · BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| | 8. BUBBLE IN POLARIZER | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| | 9. LCD'S RAINBOW COLOR | STRONG DEVIATION COLOR (OR NEWTON RING) OF LCD.....REJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA) | Minor |
| ELECTRICAL | 10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST· VOP · CHROMATICITY ... ETC) | ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA) | Critical |
| | 11.MISSING LINE | MISSING DOT · LINE · CHARACTERREJECTED | Critical |
| | 12.SHORT CIRCUIT· WRONG PATTERN DISPLAY | NO DISPLAY · WRONG PATTERN DISPLAY · CURRENT CONSUMPTION OUT OF SPECIFICATION..... REJECTED | Critical |
| | 13. DOT DEFECT (FOR COLOR AND TFT) | ACCORDING TO STANDARD OF VISUAL INSPECTION | Minor |

11.4. STANDARD OF VISUAL INSPECTION

| NO. | CLASS | ITEM | JUDGEMENT | | | | | | | | | | | | | | | | | | | | |
|------------------------|----------------------|--|---|----------------|-----------------|-----------------|------------|------------------------|------------------|---------------|---|--------|-------|-----------------|-------|---------------|-----------|--------------|----------------------|------------------|-------|------------|-------------------|
| 11.4.1 | MINOR | BLACK AND WHITE SPOT FOREIGN MATERIEL DUST IN THE CELL BLEMISH SCRATCH | <p>(A) ROUND TYPE: unit : mm.</p> <table border="1"> <thead> <tr> <th>DIAMETER (mm.)</th> <th>ACCEPTABLE Q'TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.1$</td> <td>DISREGARD</td> </tr> <tr> <td>$0.1 < \Phi \leq 0.25$</td> <td>3 (Distance>5mm)</td> </tr> <tr> <td>$0.25 < \Phi$</td> <td>0</td> </tr> </tbody> </table> <p>NOTE: $\Phi = (\text{LENGTH} + \text{WIDTH}) / 2$</p> <p>(B) LINEAR TYPE: unit : mm.</p> <table border="1"> <thead> <tr> <th>LENGTH</th> <th>WIDTH</th> <th>ACCEPTABLE Q'TY</th> </tr> </thead> <tbody> <tr> <td>-----</td> <td>$W \leq 0.03$</td> <td>DISREGARD</td> </tr> <tr> <td>$L \leq 5.0$</td> <td>$0.03 < W \leq 0.07$</td> <td>3 (Distance>5mm)</td> </tr> <tr> <td>-----</td> <td>$0.07 < W$</td> <td>FOLLOW ROUND TYPE</td> </tr> </tbody> </table> | DIAMETER (mm.) | ACCEPTABLE Q'TY | $\Phi \leq 0.1$ | DISREGARD | $0.1 < \Phi \leq 0.25$ | 3 (Distance>5mm) | $0.25 < \Phi$ | 0 | LENGTH | WIDTH | ACCEPTABLE Q'TY | ----- | $W \leq 0.03$ | DISREGARD | $L \leq 5.0$ | $0.03 < W \leq 0.07$ | 3 (Distance>5mm) | ----- | $0.07 < W$ | FOLLOW ROUND TYPE |
| DIAMETER (mm.) | ACCEPTABLE Q'TY | | | | | | | | | | | | | | | | | | | | | | |
| $\Phi \leq 0.1$ | DISREGARD | | | | | | | | | | | | | | | | | | | | | | |
| $0.1 < \Phi \leq 0.25$ | 3 (Distance>5mm) | | | | | | | | | | | | | | | | | | | | | | |
| $0.25 < \Phi$ | 0 | | | | | | | | | | | | | | | | | | | | | | |
| LENGTH | WIDTH | ACCEPTABLE Q'TY | | | | | | | | | | | | | | | | | | | | | |
| ----- | $W \leq 0.03$ | DISREGARD | | | | | | | | | | | | | | | | | | | | | |
| $L \leq 5.0$ | $0.03 < W \leq 0.07$ | 3 (Distance>5mm) | | | | | | | | | | | | | | | | | | | | | |
| ----- | $0.07 < W$ | FOLLOW ROUND TYPE | | | | | | | | | | | | | | | | | | | | | |
| 11.4.2 | MINOR | BUBBLE IN POLARIZER DENT ON POLARIZER | <p style="text-align: right;">unit : mm.</p> <table border="1"> <thead> <tr> <th>DIAMETER</th> <th>ACCEPTABLE Q'TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2$</td> <td>DISREGARD</td> </tr> <tr> <td>$0.2 < \Phi \leq 0.5$</td> <td>2 (Distance>5mm)</td> </tr> <tr> <td>$0.5 < \Phi$</td> <td>0</td> </tr> </tbody> </table> | DIAMETER | ACCEPTABLE Q'TY | $\Phi \leq 0.2$ | DISREGARD | $0.2 < \Phi \leq 0.5$ | 2 (Distance>5mm) | $0.5 < \Phi$ | 0 | | | | | | | | | | | | |
| DIAMETER | ACCEPTABLE Q'TY | | | | | | | | | | | | | | | | | | | | | | |
| $\Phi \leq 0.2$ | DISREGARD | | | | | | | | | | | | | | | | | | | | | | |
| $0.2 < \Phi \leq 0.5$ | 2 (Distance>5mm) | | | | | | | | | | | | | | | | | | | | | | |
| $0.5 < \Phi$ | 0 | | | | | | | | | | | | | | | | | | | | | | |
| 11.4.3 | MINOR | Dot Defect | <table border="1"> <thead> <tr> <th>Items</th> <th>ACC. Q'TY</th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td>$N \leq 4$</td> </tr> <tr> <td>Dark dot</td> <td>$N \leq 4$</td> </tr> </tbody> </table> <p>Pixel Define :</p>  <p>Note 1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.</p> <p>Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.</p> <p>Note 3: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.</p> | Items | ACC. Q'TY | Bright dot | $N \leq 4$ | Dark dot | $N \leq 4$ | | | | | | | | | | | | | | |
| Items | ACC. Q'TY | | | | | | | | | | | | | | | | | | | | | | |
| Bright dot | $N \leq 4$ | | | | | | | | | | | | | | | | | | | | | | |
| Dark dot | $N \leq 4$ | | | | | | | | | | | | | | | | | | | | | | |

| NO. | CLASS | ITEM | JUDGEMENT |
|---------|-------|--|--|
| 11.4.4 | MINOR | LCD GLASS CHIPPING |  $Y > S$ Reject |
| 11.4.5 | MINOR | LCD GLASS CHIPPING |  $X \text{ or } Y > S$ Reject |
| 11.4.6 | MAJOR | LCD GLASS GLASS CRACK |  $Y > (1/2) T$ Reject |
| 11.4.7 | MAJOR | LCD GLASS SCRIBE DEFECT |  <ol style="list-style-type: none"> $a > L/3$, $A > 1.5\text{mm}$. Reject B : ACCORDING TO DIMENSION |
| 11.4.8 | MINOR | LCD GLASS CHIPPING (ON THE TERMINAL AREA) |  $\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject |
| 11.4.9 | MINOR | LCD GLASS CHIPPING (ON THE TERMINAL SURFACE) |  $Y > (1/3) T$ Reject |
| 11.4.10 | MINOR | LCD GLASS CHIPPING |  $Y > T$ Reject |

12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizers which easily be damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happens by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module uses C-MOS LSI drivers, so we recommend that you:

Connect any unused input terminal to POWER or GROUND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
[It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen which is not specified in this specifications?
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD